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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *ST*  
 This application is a 371 of PCT/JP03/13514 10/22/2003

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *ST*  
 JAPAN 2002-307478 10/22/2002

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	JAPAN	8	11	2
Examiner's Signature <i>Hanaka</i> Initials <i>ST</i>				

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## TITLE

Pasted soi substrate, process for producing the same and semiconductor device

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